Amendments to the Claims

Claim 1–11 (canceled)

Claim 12 (Currently amended). A method of identifying a clock frequency of a system clock for the configuration of a peripheral device, the method comprising:

a) providing a secondary clock at a predetermined clock frequency;

b) applying the system clock and the secondary clock to a host;

c) applying the system clock and the secondary clock to the peripheral device;

d) determining the clock frequency of the system clock in the peripheral device using the second clock and based on the predetermined clock frequency; and

e) configuring the peripheral device using the determined system clock;

wherein the clock frequency of the system clock is variable at predetermined clock frequencies and further comprising a step of employing the host to determine the clock frequency after an initialization phase; and

wherein step d) further comprises determining the clock frequency with a tolerance in the peripheral device, and then comparing, in the peripheral device, the determined clock frequency with the tolerance to a table of possible frequency values and selecting in the peripheral device the clock frequency of the system clock based on the comparison.

Claim 13 (Previously presented). The method as claimed in claim 12, wherein step d) further comprises

counting a number of edge changes of the system clock within a predetermined number of periods of the secondary clock.

Claim 14 (Previously presented). The method as claimed in claim 13, wherein step e) further comprises setting an identical interface transmission rate for a first interface of the host and for a second interface of the peripheral device as a function of the clock frequency of the determined system clock.

Claim 15 (Previously presented). The method as claimed in claim 14, wherein step e) further comprises setting the interface transmission rate to an interface transmission rate defined by a predetermined standard.

Claim 16 (Previously presented). The method as claimed in claim 14, wherein further comprising:

after an initialization phase, employing the host to change the system clock; and

signaling information representative of the change to the peripheral device through the second interface.

Claim 17 (Previously presented). The method as claimed in claim 12, wherein step d) further comprises employing tolerances of both the system clock and the second clock to determine the clock frequency of the system clock in the peripheral device.

Claim 18 (Previously presented). The method as claimed in claim 12, wherein a transmission rate of data transmission between a first interface of the host and a second interface of the peripheral device is dependent on the clock frequency of the system clock.

Claim 19 (cancelled).

Claim 20 (cancelled).

Claim 21 (Currently amended). The method as claimed in claim 12 ~~claim 11~~, further comprising employing a PLL circuit in the peripheral device to generate a constant clock frequency from the clock frequency of the system clock.

Claim 22 (Previously presented). The method as claimed in one of the preceding claims, further comprising providing a Bluetooth module as the peripheral device, the Bluetooth module configured for the system clock of a mobile radio device.

Claim 23 (Currently amended). A method of identifying a clock frequency of a system clock for the configuration of a peripheral device, the method comprising:

a) providing a secondary clock at a predetermined clock frequency;

b) applying the system clock and the secondary clock to a host;

c) applying the system clock and the secondary clock to the peripheral device;

d) determining the clock frequency of the system clock in the peripheral device using the second clock based on the predetermined clock frequency; and

e) setting an identical interface transmission rate for a first interface of the host and for a second interface of the peripheral device as a function of the clock frequency of the determined system clock;

wherein step d) further comprises determining the clock frequency with a tolerance in the peripheral device, and then comparing, in the peripheral device, the determined clock frequency with the tolerance to a table of possible frequency values and selecting in the peripheral device the clock frequency of the system clock based on the comparison.

Claim 24 (Previously presented). The method as claimed in claim 23, wherein step d) further comprises

counting a number of edge changes of the system clock within a predetermined number of periods of the secondary clock.

Claim 25 (Previously presented). The method as claimed in claim 24, wherein step e) further comprises setting the interface transmission rate to an interface transmission rate defined by a predetermined standard.

Claim 26 (Previously presented). The method as claimed in claim 24, wherein further comprising:

after an initialization phase, employing the host to change the system clock; and

signaling information representative of the change to the peripheral device through the second interface.

Claim 27 (Previously presented). The method as claimed in claim 23, wherein step d) further comprises employing tolerances of both the system clock and the second clock to determine the clock frequency of the system clock in the peripheral device.

~~Claim 29~~ Claim 28 (Currently amended). The method as claimed in claim 23, wherein a transmission rate of data transmission between a first interface of the host and a second interface of the peripheral device is dependent on the clock frequency of the system clock.

~~Claim 30~~ Claim 29 (Currently amended). The method as claimed in claim 23, wherein the clock frequency of the system clock is variable at predetermined clock frequencies and further comprising a step of employing the host to determine the clock frequency after an initialization phase.

Claim 31 (Cancelled).